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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/526,278	03/01/2005	Christian Block	14219-079US1 P2002.0828 U	6665
26161	7590	11/06/2006	EXAMINER	
FISH & RICHARDSON PC P.O. BOX 1022 MINNEAPOLIS, MN 55440-1022			BAUER, SCOTT ALLEN	
			ART UNIT	PAPER NUMBER
			2836	

DATE MAILED: 11/06/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

SF

Office Action Summary	Application No.	Applicant(s)
	10/526,278	BLOCK ET AL.
	Examiner	Art Unit
	Scott Bauer	2836

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extension of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) Responsive to communication(s) filed on 21 September 2006.
- 2a) This action is FINAL. 2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) Claim(s) 22-44 is/are pending in the application.
 - 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) Claim(s) _____ is/are allowed.
- 6) Claim(s) 22-44 is/are rejected.
- 7) Claim(s) _____ is/are objected to.
- 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on 01 March 2005 is/are: a) accepted or b) objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--------------------------------------------------------------------------------------------------------------------------------|-------------------------------------------------------------------|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____. |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)
Paper No(s)/Mail Date <u>9/21/06</u> | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| | 6) <input type="checkbox"/> Other: _____. |

DETAILED ACTION

Claim Rejections - 35 USC § 103

1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

2. Claims 22-24, 26-32 & 41-44 are rejected under 35 U.S.C. 103(a) as being unpatentable over Kodim (WO 00/57515) in view of Glaser et al. (US 5,896,265).

3. With regard to Claim 22, Kodim, in Figure 1b teaches circuitry comprising: a terminal (11) for use with a high-frequency signal; at least two signal lines (Port 2 & Port 3); a diplexer (3), which is a switching unit for connecting the terminal to a signal line; and a primary protection device (1) for protecting against electrostatic discharges (page 1 lines 3-5), the primary protection device (1) being between the terminal (11) and the switching unit (3), and that the primary protection device comprises a first element (L1) that diverts voltages.

Kodim does not teach that the primary protection device diverts a voltage greater than 200 V to a reference potential.

Glaser et al., in Figure 1, teaches a protection device for a high frequency signal, the protection device protecting against voltage surges, the primary protection device

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comprising a first element (18) that diverts voltages having a pulse height greater than a voltage level to a reference potential (24) (column 3 lines 25-37)).

It would have been obvious to one of ordinary skill in the art at the time the invention was made to combine the teachings of Kodim with Glaser et al., by incorporating the protection circuit taught by Glaser et al. into the circuit taught by Kodim, for the purpose of providing a device that protects equipment from the effects of fast rising transients which pass through an impedance means or which are due to back-flow transients (column 3 lines 50-54)

Kodim in view of Koss discloses the claimed invention except that the first element diverts voltages having a pulse height greater than 200 V to a reference potential. However, it would have been obvious to one of ordinary skill in the art at the time the invention was made to set the first protection device to trigger at 200 V, since it has been held that discovering an optimal value of a result effective variable involves only routine skill in the art. *In re Boesch*, 617 F.2d 272, 205 USPQ 215 (CCPA 1980).

4. With regard to Claims 23 & 24, Kodim in view of Glaser et al. discloses the circuitry of Claim 22. Kodim in view of Glaser et al. does not disclose that the first element has an insertion attenuation that is less than .3 dB and that the first element has a capacitance that is less than 1 pF (column 3 lines 31-40).

However, it would have been obvious to one of ordinary skill in the art at the time the invention was made to select the desired parameters of the device, since it has been

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held that discovering an optimal value of a result effective variable involves only routine skill in the art. *In re Boesch*, 617 F.2d 272, 205 USPQ 215 (CCPA 1980).

5. With regard to Claim 26, Kodim in view of Glaser et al. discloses the circuitry of Claim 22. Glaser et al. further discloses that the primary protection device comprises a circuit path (17) that connects a terminal (12) and a switching unit (16); and wherein the first element (18) connects the circuit path to the reference potential.

6. With regard to Claim 27, Kodim in view of Glaser et al. discloses the circuitry of Claim 22. Glaser et al. further discloses a second element (20) that is in parallel with the first element, the second element for limiting a current load of the first element.

7. With regard to Claim 28, Kodim in view of Glaser et al. discloses the circuitry of Claim 22. Glaser et al. further discloses a capacitor (44) on a circuit path between the first element (18) and the second element (20).

8. With regard to Claims 29 & 31, Kodim in view of Glaser et al. discloses the circuitry of Claim 27. Kodim in view of Glaser et al. does not disclose that the second element comprises a discharger which would have a capacitance of less than 1 pF. However, it would have been obvious to one of ordinary skill in the art at the time the invention was made to select a desired parameter of the device, since it has been held

that discovering an optimal value of a result effective variable involves only routine skill in the art. In re Boesch, 617 F.2d 272, 205 USPQ 215 (CCPA 1980).

9. With regard to Claim 30, Kodim in view of Glaser et al. teaches the circuitry of claim 22. Glaser et al. further teaches that the second element (4) comprises a varistor (column 6 lines 16-18), which is a polymer suppressor.

10. With regard to Claim 32, Kodim in view of Glaser et al. discloses the circuitry of Claim 27. Glaser et al. further discloses that the second element (20) comprises an inductive element (38) having an inductance.

Kodim in view of Glaser et al. further does not disclose that the inductive element has a value of 18 nH. However, it would have been obvious to one of ordinary skill in the art at the time the invention was made to use a 18 nH inductor in the second element, since it has been held that discovering an optimal value of a result effective variable involves only routine skill in the art. In re Boesch, 617 F.2d 272, 205 USPQ 215 (CCPA 1980).

11. With regard to Claim 41, Kodim in view of Glaser et al. discloses the circuitry of Claim 22. Kodim further discloses that a switching unit comprises a gallium arsenide switch (page 6 lines 15-17).

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12. With regard to Claim 42, Kodim in view of Glaser et al. discloses the circuitry of Claim 22. Kodim further discloses that the terminal (2) comprises an antenna input of a mobile telephone (page 1 lines 1 & 2).

13. With regard to Claim 43, Kodim in view of Glaser et al. discloses the circuitry of Claim 22. Kodim further discloses that the signal lines comprise transmitting and receiving paths of a mobile telephone (Page 4 lines 23-31 and Page 5 lines 1&2).

14. With regard to Claim 44, Kodim in view of Glaser et al. discloses the circuitry of Claim 22. Kodim further discloses that the switching unit (3) and the primary protection device (1) are integrated into a multiplayer ceramic substrate. Kodim, on page 6, lines 15-17; disclose that the protection device (1) can be built into a Transmit/Receive Switch.

15. Claim 25 is rejected under 35 U.S.C. 103(a) as being unpatentable over Kodim in view of Glaser et al. as applied to claim 22 above, and further in view of Hitachi LTD (JP 2-162744).

16. With regard to Claim 25, Kodim in view of Glaser et al. teaches the circuitry of Claim 22.

Kodim in view of Glaser et al. does not teach the first element comprises a gallium arsenide double diode.

Hitachi Ltd, in Figure 7, teaches a double diode constructed of gallium arsenide used for over-voltage protection.

It would have been obvious to one of ordinary skill in the art at the time the invention was made to combine the teachings of Kodim in view of Glaser et al. with Hitachi Ltd, by constructing the first element of Glaser et al. with gallium arsenide double diode, for the purpose of providing greater protection to the switching unit by increasing the switching speed of the protection circuit.

17. Claims 33, 35 & 39 are rejected under 35 U.S.C. 103(a) as being unpatentable over Kodim in view of Glaser et al. as applied to claim 22 above, and further in view of Kurchuk et al. (US 6272327).

18. With regard to Claim 33, Kodim in view of Glaser et al. teaches the circuitry of Claim 22.

Kodim in view of Glaser et al. does not teach that the circuit paths that provide control signals to the switching unit, each of the circuit paths comprising a secondary protection device against electrostatic discharges.

Kurchuk et al., in Figure 2, teaches a high power wireless telephone with over-voltage protection, comprising circuit paths (36 & 38) that provide control signals to the switching unit (24), each of the circuit paths comprising a secondary protection device (44 & 46) against electrostatic discharges (column 3 lines 66 & 67 & column 4 lines 1-4).

It would have been obvious to one of ordinary skill in the art at the time the invention was made to combine the teachings of Kodim in view of Glaser et al. with Kurchuk et al., by Replacing the diplexer (3) taught by Kodim, with the switching unit taught by Kurchuk, for the purpose of providing a mobile phone with a switching unit that prevents receiver overloading without degrading the receiver sensitivity (Kurchuk et al. Column 2 lines 31-35).

19. With regard to Claim 35, Kodim in view of Glaser et al. and further in view of Kurchuk et al. discloses the circuitry of Claim 22. Kurchuk further discloses that the switching units comprises field effect transistors (Q1 & Q2), a contact break distance of each of the field effect transistors connecting the terminal (30) to the signal line (32 & 34), and wherein the circuitry further comprises: circuit paths that connect to gates of the field effect transistors, the circuit paths (40 & 42) for providing control signals to the gates, each of the circuit paths comprising a secondary protection device (44 & 46) for protecting against electrostatic discharges.

20. With regard to Claim 39, Kodim in view of Glaser et al. and Kurchuk et al. discloses the circuitry of Claim 35. Kurchuk further discloses that at least one secondary protection device (44) is connected to the reference potential.

21. Claim 34 is rejected under 35 U.S.C. 103(a) as being unpatentable over Kodim in view of Glaser et al. as applied to claim 22 above, and further in view of Toshiba (JP 02000134945).

22. With regard to Claim 34, Kodim in view of Glaser et al. teaches the circuitry of Claim 22.

Kodim in view of Glaser et al. does not teach that the circuitry further comprises a circuit path for supplying for an operating voltage to the switching unit, the circuit path comprising a secondary protection device for protecting against electrostatic discharges.

Toshiba, in Figure 1, teaches a surge protection circuit for a switching unit (4) the circuit path supplies an operating voltage to the switching unit and the path comprises a protection device (6) for protecting against electrostatic discharges.

It would have been obvious to one of ordinary skill in the art at the time the invention was made to combine the teachings of Kodim in view of Glaser et al. with Toshiba, by protecting power terminal of the switching network taught by Kodim, with the protection device taught by Toshiba for the purpose of bypassing surge currents and voltages in the event of a line fault.

23. Claims 36-38 are rejected under 35 U.S.C. 103(a) as being unpatentable over Kodim in view of Glaser et al. and Kurchuk as applied to claim 33 above, and further in view of Ikeda et al. (US 5276422).

24. With regard to Claims 36-38, Kodim in view of Glaser et al. and Kurchuk teaches the circuitry of claim 36. Kurchuk et al. further teaches that a low pass filter comprising a capacitor and a resistor can be used to absorb transient surge voltages.

Kodim in view of Glaser et al. and Kurchuk et al. does not teach that the voltage limiting element comprises a varistor or a zener diode having a switching voltage that is less than 100 V.

Ikeda, teaches a device to protect a load. The device contains an element (14) that absorbs surge voltages. Ikeda further teaches that the element (14) can be a CR filter, a varistor or a zener diode (column 3 lines 66-68 & column 4 lines 1-6). Ikeda also discloses that as an example, the element can discharge at 300V (column 5 lines 1-3).

It would have been obvious to one of ordinary skill in the art at the time the invention was made to combine the teachings of Kodim in view of Glaser et al. and Kurchuk et al. with Ikeda, by replacing the CR filter taught by Kurchuk et al. with the varistor or zener diode taught by Ikeda, for the purpose of providing a voltage limiting element with a fast reaction time that increases the switching speed of the switching unit.

Further, Kodim in view of Glaser et al. and further in view of Kurchuk and Ikeda discloses the claimed invention of Claim 36 except that a discharge voltage of 300 V is specified, instead of 100 V. It would have been obvious to one of ordinary skill in the art at the time the invention was made to set a discharge voltage of 100 V, since it has

been held that discovering an optimal value of a result effective variable involves only routine skill in the art. *In re Boesch*, 617 F.2d 272, 205 USPQ 215 (CCPA 1980).

25. Claim 40 is rejected under 35 U.S.C. 103(a) as being unpatentable over Kodim in view Glaser et al. as applied to claim 22 above, and further in view of Trikha et al. (US 6,072,993)

26. With regard to Claim 40, Kodim in view of Glaser et al. discloses the circuitry of Claim 22.

Kodim in view of Glaser et al. does not teach that the switching unit comprises PIN diodes.

Trikha, in Figure 3A, teaches a diplexer for a cellular phone wherein the switching element comprises pin diodes (116', 118', 120', & 122').

It would have been obvious to one of ordinary skill in the art at the time the invention was made to combine the teachings of Kodim in view of Glaser et al. with Trikha, by replacing the switching unit taught by Kodim with the diplexer taught by Trikha, for the purpose of using the device in high frequency applications.

Response to Arguments

27. Applicant's arguments with respect to claims 22-24, 26-29, 31, 32 &41-44 has been considered but are moot in view of the new ground(s) of rejection.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Scott Bauer whose telephone number is 571-272-5986. The examiner can normally be reached on M-F 9am-6pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Brian Sircus can be reached on 571-272-2058. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

SAB
29 OCT 06

Stephen W. Jackson
10-30-06

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PRIMARY EXAMINER